

**GURU GHASIDAS VISHWAVIDYALAYA BILASPUR**  
**(A Central University)**

**(Established by Central Universities Act 2009 No.25 Of 2009)**



**Certificate**

This is to be certified that Abhay masih student of BSC VI Semester, Department of Pure and Applied Physics, Guru Ghasidas Vishwavidyalaya Bilaspur, have completed his Project entitled "FULL ADDER IMPLEMENTATION WITH BREAD BOARD". He has submitted his Project Report for the partial fulfillment of the curriculum of the Degree of BSC Electronics VI SEM from GGV. During this project work he has learned about IC 7486, IC 7408, Breadboard, connecting wires, Led's, power supply, resister etc. I recommend the project report to be forwarded for evaluation. I wish his all success in his life and career.

*inbipathi*

विभागाध्यक्ष/H.O.D.

शुद्ध एवं अनुप्रयुक्त भौतिकी विभाग  
Dept. of Pure & Applied Physics

गुरु घासीदास विश्वविद्यालय  
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**Submitted By:**

Abhay masih  
BSc. (Hon's) Electronics  
VI semester  
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**Guided By:**

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